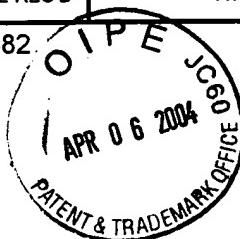




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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE RECD	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/719,119	11/21/2003	2814	482	51889/3	14	26	4



CONFIRMATION NO. 1799

FILING RECEIPT



OC000000011948354

Date Mailed: 02/23/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Douglas R. Hackler SR., Boise, ID;
 Stephen A. Parke, Nampa, ID;

Domestic Priority data as claimed by applicant

10/613,169 July 3, 2003

Foreign Applications

If Required, Foreign Filing License Granted: 02/23/2004

Projected Publication Date: 05/26/2005

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Double-gated transistor circuit

Preliminary Class

RECEIVED

FEB 25 2004

STOEL RIVES, S.L.C.



257

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)

Applicant(s): Douglas R. Hackler, Sr. et al.

Docket No.

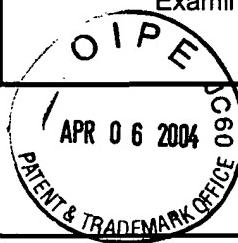
51889/3

Serial No.
10/719,119Filing Date
November 21, 2003

Examiner

Group Art Unit
2814

Invention: DOUBLE-GATED TRANSISTOR CIRCUIT



I hereby certify that this Response to Request for Corrected Filing Receipt (1 pg.); (see below)
(Identify type of correspondence)

is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 1, 2004
(Date)

John R. Thompson*(Typed or Printed Name of Person Mailing Correspondence)**John R. Thompson**(Signature of Person Mailing Correspondence)*

Note: Each paper must have its own certificate of mailing.

Utility Patent Application Transmittal (1st page only)
Request for Corrected Filing Receipt (1 pg.)
Copy of Official Filing Receipt (2 pgs.)
Postcard



April 1, 2004

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VIA FIRST CLASS MAIL

Office of Initial Patent Examination's
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Commissioner of Patents
United States Patent and Trademark Office
Washington, D.C. 20231

**Re: Response to Request for Corrected Filing Receipt
U.S. Patent Application**

Title: DOUBLE-GATED TRANSISTOR CIRCUIT
Serial No.: 10/719,119
Filed: November 21, 2003
Inventor(s): Douglas R. Hackler, Sr. and Stephen A. Parke
Our File No.: 51889/3

Dear Sirs:

Pursuant to 37 CFR § 1.76(c), the Applicant is providing a supplemental Application Data Sheet. The priority claim is amended to specify that the pending application is a continuation-in-part of prior application number 10/613,169. All previously submitted information has not changed.

Filed concurrently with this paper, is a request for corrected filing receipt. As the Application Data Sheet has been properly amended, the Applicant respectfully requests a corrected filing receipt.

Sincerely,

A handwritten signature of John R. Thompson in black ink.

John R. Thompson

JRT/jlw
Enclosures

UTILITY PATENT APPLICATION TRANSMITTAL
(Small Entity)

Docket No.
 51889/3

Total Pages in this Submission



Only for new nonprovisional applications under 37 CFR 1.53(b))

TO THE COMMISSIONER FOR PATENTS

Mail Stop Patent Application

P.O. Box 1450

Alexandria, VA 22313-1450

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

DOUBLE-GATED TRANSISTOR CIRCUIT

and invented by:

Douglas R. Hackler, Sr. and Stephen A. Parke

If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:

Continuation Divisional Continuation-in-part (CIP) of prior application No.: 10/613,169

Which is a:

Continuation Divisional Continuation-in-part (CIP) of prior application No.: _____

Which is a:

Continuation Divisional Continuation-in-part (CIP) of prior application No.: _____

Enclosed are:

Application Elements

1. Filing fee as calculated and transmitted as described below
2. Specification having twenty-six (26) pages and including the following:

- a. Descriptive Title of the Invention
- b. Cross References to Related Applications (*if applicable*)
- c. Statement Regarding Federally-sponsored Research/Development (*if applicable*)
- d. Reference to Sequence Listing, a Table, or a Computer Program Listing Appendix
- e. Background of the Invention
- f. Brief Summary of the Invention
- g. Brief Description of the Drawings (*if filed*)
- h. Detailed Description
- i. Claim(s) as Classified Below
- j. Abstract of the Disclosure



April 1, 2004

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Office of Initial Patent Examination's
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Washington, D.C. 20231

**Re: Request for Corrected Filing Receipt
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Title: DOUBLE-GATED TRANSISTOR CIRCUIT
Serial No.: 10/719,119
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Inventor(s): Douglas R. Hackler, Sr. and Stephen A. Parke
Our File No.: 51889/3

Gentlemen:

Enclosed is an official filing receipt. We have noted the following error:

1. The "Domestic Priority data as claimed by applicant" is missing. It should indicate the following information: 10/613,169 July 3, 2003.

Please issue a corrected filing receipt rectifying this error. Thank you for your assistance in this matter.

Very truly yours,

A handwritten signature of John R. Thompson.

John R. Thompson

JRT/jlw
Enclosure